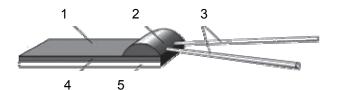
## Miniaturized Platinum temperature sensors with high resistance for Smart Systems

Dr. Kiesewetter, Olaf UST Umweltsensortechnik GmbH Dieselstr. 2, 98716 Geschwenda

The development and realization of smart system for applications in aerospace industry, automotive industry, security engineering, logistics, medical engineering a.o. put new and extended requirements to sensors and sensor systems. The optimized adaptation of the sensor system to the measurement objective, especially with regard to miniaturization and integration in overall system concepts, reduction of power consumption, improvement of measurement accuracy, reduction of response times, extension of the temperature range, long term stability and reliability are only a few of the requirements to temperature sensors.

UST Umweltsensortechnik GmbH as a leading manufacturer of innovative ceramic sensor technology for temperature and gas measurements masters these challenges successfully by the visionary as well as market-driven and application-oriented development and production of Platinum-Thinfilm-temperature sensor elements and custom designed semi-finished temperature probes.

The structural design of a ceramic Pt-Thinfilm-Temperature sensor element is shown in figure 1. Simplified a sensor element consists of a ceramic carrier with a structured platinum layer, covered with a passivating layer and the connecting leads.



- 1... passivating layer
- 2... locking layer
- 3... connection leads
- 4... structured platinum layer
- 5... Al<sub>2</sub>O<sub>3</sub>-carrier substrate

Figure 1: Structural design of a ceramic Thinfilm-Temperature sensor element

Currently, the manufacturing of platinum structures of micro-structured Pt-Thinfilm-Temperature sensors is done with solid-state lasers. By using this technology 10 µm structures can be created. This results in a minimum chip size of 5 mm² for a Pt1000 temperature sensor - the chip size of a Pt10000 temperature sensor would be about 35 mm². Consequently, the manufacturing of highest resistance temperature sensors with industry-strength chip dimensions applying this technology will not be possible.

On initiative of UST Umweltsensortechnik GmbH in collaboration with Fraunhofer IPM in Freiburg an innovative Lift-off Technolog for the structuring of platinum on ceramics substrates was developed. This photolithographic procedure enables the creation of minimum web thicknesses and electrode distances for platinum temperature sensors with chip sizes less then 1 mm².

Between 2006 and 2008, a clean-room was set-up and the photolithographic procedure was implemented at the facilities of UST Umweltsensortechnik GmbH. This investment was supported by the Free State of Thuringia <sup>1)</sup>. The clean room meets the requirements of the clean-room class ISO2 according to ISO14644-1 (class 100 according to obosolete US fed. Std. 209E) and provides the environment for the manufacturing of smallest micro-structures.

These technical and technological prerequisits have been and continue to be fundamental to the successful realization of further product innovations. One of these innovations is the miniaturization of high-resolution temperature sensors for the application in RFID systems. Within the scope of the project TRACK <sup>2)</sup> supported by the Federal Ministry of Education and Research UST Umweltsensortechnik GmbH has developed a micro-structured SMD temperature sensor (according to DIN EN 60751) with chip size of about 4 mm² only (figure 2).

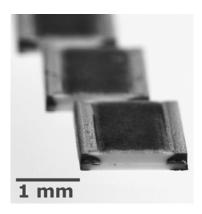


Figure: 2: SMD Pt10000 (chip area appr. 4 mm²)

The major techological challenge was basically the structuring of smallest platinum tracks (about  $3\mu m$ ) on a, compared to slilicon-wafer, very rough ceramics surface (figure 3). As any dust partice existing in the micro-structuring process will result in one or more defect chips, the creation of structures of this kind is only possible in a clean room, nearly free of any particle. The micro-structuring of the platinum is carried out by clean-room processes like Photolithography, Wet-Chemical Etching and Lift-Off-Procedures that are typically used in the silicon technology, only.

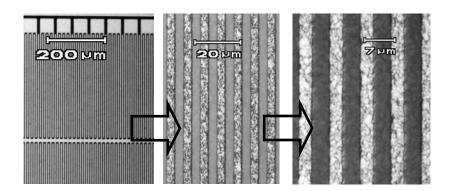


Figure 3: Pt-Structure of a Pt10000 on a ceramic substrat with a "rough surface"

During the project these thechnologies could be successfuly transferred to ceramics-wafers, the standard-substrates used by UST by default. The application of this technology enables a more efficient manufacturing of temperature sensor elements by e.g. the reduction of productions steps and higher production yields.

The advantages of highest-resistance temperature sensors compared to current standard temperaturesensors are the following:

The temperature-sensitive change of resistance of a Pt10.000, for instance, is higher by factor 100 compared to a Pt100. An evaluation electronics can resolve the change of resistance more detailed, resulting in a more precize measurement result.

Furthermore, due to a higher voltage stroke a lower measurement current is necessary. Thus, highest-resistance temperature sensors have a lower power consumption - the electrical power of a measurment with a Pt100 is about 900  $\mu$ W, with a Pt10000 merely about 25  $\mu$ W. Accordingly, the energy demand is many times lower. This issue becomes more and more important, especially, in self-sustaining systems with a limited energy storage, but long-term measurment periods.

The contact resistance of the connectors is another important issue on the set-up of measurement systems, even on the integration of sensors. At an Pt10000 this resistance is low in relation to the actual sensor resistance and doesn't have to be regarded. Consequently, the sensor can be connected to the measurement system by a 2-wire cable. Expansive 3- or 4-wire circuts, used for compensation of the

wire-resistance are no longer nessecary. This results in a reduction of components and costs in measurment systems.

The photolithographic technology enables the manufacturing of new generation highest-resistance temperature sensors by UST Umweltsensortechnik GmbH. Additionally, even the currrently offered temperature sensors can be produced in smaller chip dimensions (e.g. Pt1000 on 1 mm²). They will be provided with the Standard-Temperature-Coefficient TC 3850 ppm/K as well as TC 3750 ppm/K.

UST Umweltsensortechnik GmbH was established in 1991 supported by Federal Ministry of Education and Research (TOU-program). Today, UST Umweltsensortechnik GmbH is a medium sized company, successfully acting in the global market.

The product range includes:

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Platinum temperature sensor elements based on a ceramic chip with a thin film laser structured platinum layer (temperature range from -200 to +1000°C) in various R <sub>0</sub> -values (Pt 10 Pt 10000), shapes, dimensions and elements with custom-designed leads;
Custom designed semi finished temperature probes from -100°C to +1000°C (screw-in temperature probes, plug-in temperature probes, immersion temperature probes);
MOS gas sensor elements for a wide range of applications, i.e. environmental technology, automotive applications, leak detection, location of smouldering underground fires, detection of lower explosive limit (LEL), air quality monitoring and process measurement. Various sensor types for the detection of CO, $H_2$ , $C_2H_5OH$ , $CH_4$ , $NO_2$ , $O_3$ , $NH_3$ , hydrocarbons ( $C_XH_X$ from $C_1$ to $C_8$ ), R134a a. o. are available. Also gas sensor arrays with a measuring range depending on the gas from some ppb to the Vol%-Range and a patented TRIPLE-sensor-module with artificial intelligence are offered.
Portable gas detectors for the quick and selective detection/determination of different gas types (Hydrogen, CO <sub>2</sub> , Methane, Refrigerants a. o.);
Stationary detectors for combustible gases and selected VOC markers in the range of a few ppm up

Main application fields of the products are automotive engineering, industrial process measurement, energy technology, environmental technology, safety engineering and medical engineering. UST Umweltsensortechnik GmbH is certified according to ISO/TS 16949:2002, DIN EN ISO 14001:2005, ATEX RL 94/9/EG Annex IV DIN EN 13980:2003.

<sup>&</sup>lt;sup>1)</sup> Project: Einführung der Photolithographietechnologie (Project reference: 2005 TE 0220)





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<sup>2)</sup> Cooperative research project: TRACK (Project reference: 16SV2037)

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