Energy Harvesting for Sensor Applications in the Energy Sector

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Abstract:

Because of the energy turnaround the power industry will be confronted with a massive restructuring. Only by an intelligent control prospectively it is possible to obtain a reliable energy supply. Wireless sensor systems can help to provide necessary information available as control variables. This paper presents a robust approach to use the electrostatic field of electrical equipment, for the supply of wireless sensor systems.

Key words: Energy harvesting, dc/dc converter, wireless sensing, power line monitoring

Introduction

A huge number of additional measured parameters of the operating grid [1], [2] became necessary to control the energy flow, the energy distribution or the main behavior of components of the transmission network in the future. Generally, instrumentation in such environments evokes heavy technical and monetary efforts. Within this context, a wireless sensor system accompanied by a self-organized power supply presents a promising alternative [3].

Analyzing system components of wireless sensor systems should be noted, wireless communication is already commercially available for wide spectra of applications [4]. Available methods for an autonomous power supply, like solar cells or thermo electrical converters, do not have a required reliability or availability [5]. Even though, in most cases the requirements for the rate of measurements foster the method using an ambient energy source [6]. More critical requirements for the system design of such a sensor approach results from the operation as an autonomous system. Most important, the system has to start-up reliably without any multiple charges. It’s applicable as well to the generation of a control signal for necessary converter architecture [7], [8].

In this paper, a unique approach for an autonomous power supply will be proposed using the quasistatic field of electrical equipment of the grid.

Energy Source Description

The use of the quasistatic field of electrical equipment provides some advantages especially in the focused field of application.

Fig. 1. Cylindrical structure to realize a capacitive voltage divider.

The quasistatic field is continuously available with a frequency of 50 Hz under high voltage. In case of the energy distribution, the voltage is constant regarding the voltage level of the distributing system. However, it is possible to use the electrostatic field by a capacitive structure which is shown in figure 1 as an exemplary for a cylindrical structure [9]. A separate electrode around the conductor realizes a capacitive voltage divider. It consists of a capacitance between the conductor and the electrode which is used later on as source and another capacitance between the electrode and ground.
towards the ground. The voltage divides inversely proportional according to the ranges of the capacitors, as described in equation 1,

$$C_{cyl} = \frac{Q}{U_E} = \oint \frac{\bar{D} \cdot d\bar{A}}{E \cdot dr} = \frac{2\pi \varepsilon_0 l}{\ln \frac{R_2}{R_1}}$$  \hspace{1cm} (1)$$

The dependence on the small capacitance and the low frequency make the source impedance of the used capacitance leading to the range of some hundred mega ohms. An equivalent voltage source [10] is used to describe the capacitive voltage divider as shown in figure 2. It also illustrates an analytical description of the electrical behavior that results into a normalized curve of the available power over the impedance. The technological realization is not currently possible when impedance coincide with a maximal available power. The challenge of using such high ohmic current sources is to develop converter architectures. That can realize adapted input impedances with high input voltage in order to enable the power demand of an energy autonomous system.

**Electronic Design**

Various converter topologies were evaluated [11] in order to develop converter architectures as well as to realize sufficient input impedance. A forward converter topology was identified as a promising approach. This’s because of the ability of making an adequate control signal at the first side. As described in equation 2, specific input impedance is realizable.

$$Z_m = \frac{2 \cdot L}{T \cdot D^2} \cdot \frac{1}{1 - \frac{U_{out}}{U_i}}$$  \hspace{1cm} (2)$$

$$D.$$ duty cycle

$$T.$$ period

In addition, as already mentioned, a reliable start-up mechanism a passive generation of a control signal are demanded for the operation as a power supply for an energy autonomous system, as show in figure 3. Both components had to be placed at the primary side supplied by the input voltage of the system. A charge with less state at secondary side prevents the generation of a necessary control signal after long term interruptions.

![Fig. 3. Schematic of the forward converter topology including additional components.](image)

**A. Control signal generation**

The figure 4 illustrates that minor requirements for the control signal characteristics of a forward converter enables the realization of the signal using a passive oscillating. The control transformer $TR_1$ consists of one primary and two inverse secondary windings. Using saturation state of the core material, a periodical changing of the magnetization can be realized. Also it changes voltage signals at the secondary side of the transformer. These signals serve as control signal for the transistors $T_1$ and $T_2$. The switching frequency, as well as the voltage level of the $TR_1$ output signal is adjustable by winding and inductivity ratio.

![Fig. 4. Sectional schematic of the oscillating circuit including switching elements with separate gate-voltage protection](image)
B. Start-up mechanism

A reliable long term operation including source interruptions involves a passive start-up mechanism of the converter architecture. The operation at high voltage (<500V) and low current (~µA) represent the further design requirements. The influence on the input impedance and a passive behavior in normal operation is negligible.

Fig. 5. Sectional schematic of the DIAC controlled start-up mechanism.

A major component of the start-up mechanism is the DIAC DI1 as shown sectional in figure 5. The capacitor C2 is slowly charged by a high ohmic resistor R1 until the trigger voltage of DI1 is reached and switched on. Afterwards the low side transistor T2 will be activated and the oscillation will be running. There, where D6 is limiting the voltage of C2 so that DI1 doesn’t reach the trigger voltage again in normal operation of the circuit.

C. Impedance matching

The transformer TR2 ensures the impedance matching as shown in figure 3. A specific capacitor separates the ac signal from a dc offset. In fact, a defined output signal is adjustable by the winding ratio of TR2. That transformer is responsible to realize a galvanic separation in order to protect sensitive components such as micro-controllers or RF-architectures.

Circuit Evaluation

A converter test prototype was evaluated in a laboratory test environment under high voltage power supply to benchmark the circuit. The main feature of the system is to function as a power supply for an energy autonomous system. The figure 6 illustrates the circuit characterization which is a sequence of the output signal of TR1. A switching frequency appears to be stable in the range of \( f_s = 200 \text{ kHz} \). An asymmetric behavior occurs because of the usage of a third test winding at TR1 secondary side without voltage protection.

Fig. 6. Measured timing of the control transformer output signal.

The analysis in the high voltage test field was carried out using sensor housing as shown in figure 7. The necessary electrode to build up the capacitive voltage divider is realized by a metalized inner side of the housing. Using a fiber-optic cable system, it is possible to make a characterization directly at a power line section under high voltage.

Fig. 7. Sensor housing for a laboratory test environment under high voltage

Figure 8 shows as an example, the charge timing of a storage capacitor \( C_{DLC} \) at two different voltage levels \( U_0 \). This timing is used to evaluate the continuous output power \( P_C \) of the converter using the equation 3:

\[
P_C = \frac{1}{2} \cdot C_{DLC} \cdot \frac{(U_{DLC,N+1}^2 - U_{DLC,N}^2)}{\Delta t}
\] (3)

The measured curve indicates a discontinuous operation of the converter. This behavior appears if the charge of the source is too strong which leads to a voltage breakdown in the capacitor \( C_1 \). It is charging again without converter operation.

By reaching the DI1 trigger voltage the converter is starting up again. A higher input voltage
leads to a higher frequency of this cycle and enables DC output power of approximately $P_c \approx 2 \text{ mW}$. A continuous converter operation is realizable by increasing the input impedance of the converter as well as it increases the available output power. Because of a significant better behavior in the high voltage test field (for this method) in comparison to the behavior at a real overhead power line, the laboratory level of 60 kV correlates approximately with the voltage level of a 110 kV system.

Fig. 8. Charge timing of a storage capacitor to evaluate the continuously output power.

Conclusion

Within this contribution a unique approach of an adapted forward converter topology is presented to use the electrostatic field of electrical equipment. The functionality was successfully demonstrated using a cylindrical sensor housing mounted on a power line section in a high voltage test field. A DC output power of approximately $P_c = 2 \text{ mW}$ was evaluated, already sufficient to supply specific wireless sensor networks. Optimization approaches to increase the available power were addressed. Finally there is a novel and robust method available to supply a wireless sensor system in the harsh environment of electrical equipment in the field of power generation and distribution. This method has the ability to reduce the efforts of instrumentation and especially for long term maintenance of such systems.

References